

2016 CIGRE SESSION - SC B4 SCHEDULE

Events	Description	Chair/Convener	Sunday	Monday	Tuesday	Wednesday	Thursday	Friday
			21-Aug	22-Aug	23-Aug	24-Aug	25-Aug	26-Aug
Contributors' Meeting		Special Reporters Nalin Pahalawaththa Carl Barker			All Day Room 362			
Discussion Meeting		Mohamed Rashwan				All Day Bleu (Level 2)		
Poster Session		Moderator/Chairman Kamran Sharifabadi					2:30-6:00pm Hall Maillot	
SC Meeting		Mohamed Rashwan						All Day Room 241
B4.62 Workshop "Connection of Wind Farms to weak AC Network"		Nalin Pahalawaththa					8:30am-12:40pm Room 253	
Tutorial for Young Engineer		Mojtaba Mohaddes		4:30-6:00pm Room TBD				
AG01	Strategic advisory group	Mohamed Rashwan		2:30-4:00pm Room 223				
AG04	HVDC system performance	Narinder Dhaliwal	9:00am-12:30pm Room 326&327					
B4B5.59	Control and Protection of HVDC Grids.	Kees (C.G.A.) Koreman		All Day Room 131				
B4.63	Commissioning of VSC HVDC Schemes	Les Brand	Morning Room 121					
B4.64	Impact of AC System Characteristics on the Performance of HVDC schemes	Jef Beerten		All Day Room 242A				
A3B4.34	Technical requirements and specifications of state-of-the-art DC switching equipment	Christian Franck				Morning Room 126&127		
B4.66	Implications for harmonics and filtering of the staggered installation of HVDC converter stations in proximate locations	Fernando Cattan Jusan		All Day Room 125				
B4.67	Harmonic aspects of VSC HVDC, and appropriate harmonic limits	Nigel Shore					All Day Room 131	
B4.68	Revision of Technical Brochure 92 - DC Harmonics and Filtering of HVDC schemes	Nigel Shore			All Day Room 301			
B4.69	Minimizing loss of transmitted power by VSC during Overhead line fault	Dennis Woodford	9:00am - 3:00pm Room 134&135					
B4.70	Guide for electromagnetic transient studies involving VSC converters	Sébastien Denetière					All Day Room 315	
B4.71	Application guide for insulation coordination of VSC converter stations	Mojtaba Mohaddes			All Day Room 330 & 331			
B4.72	DC grid benchmark models for system studies	Ting An					All Day Room 124 & 125	
B4B1C4.73	Surge and extended overvoltage testing of HVDC Cable Systems	Markus Saltzer				All Day Room TBD		